

EG31M

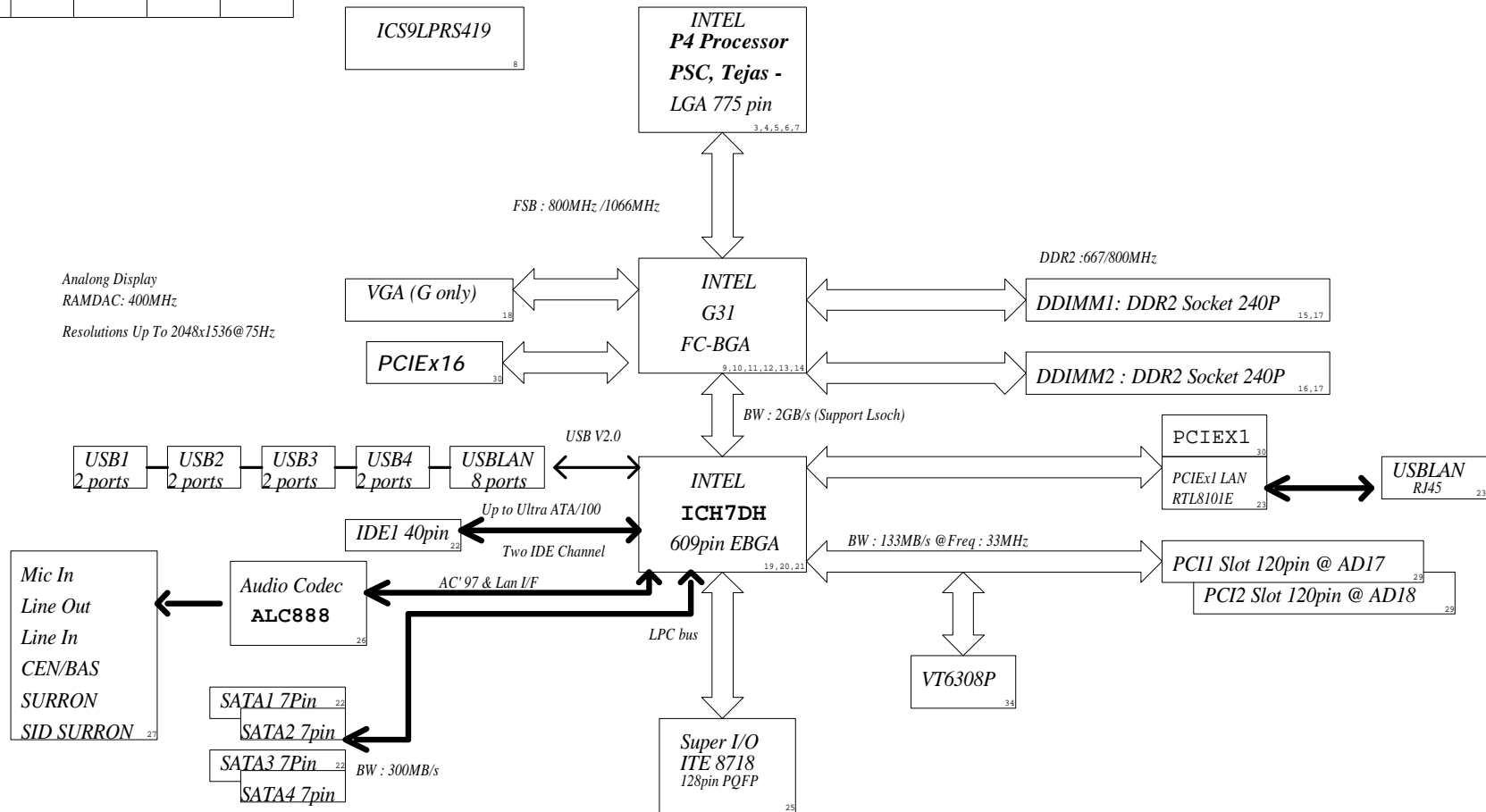
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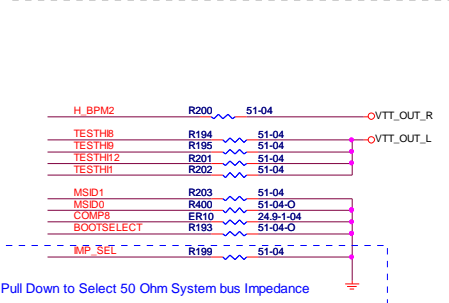
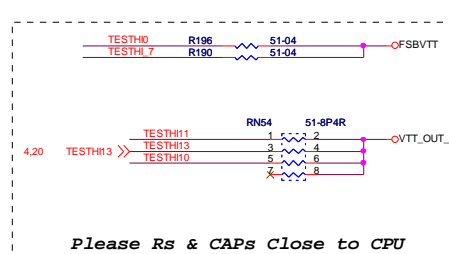
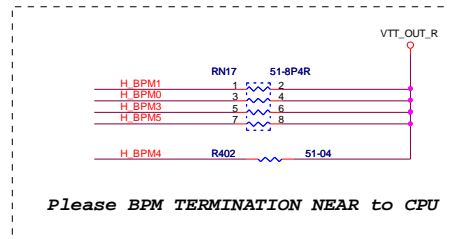
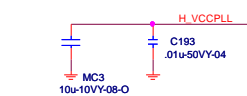
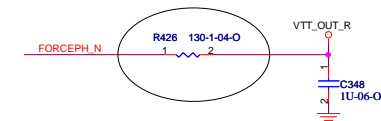
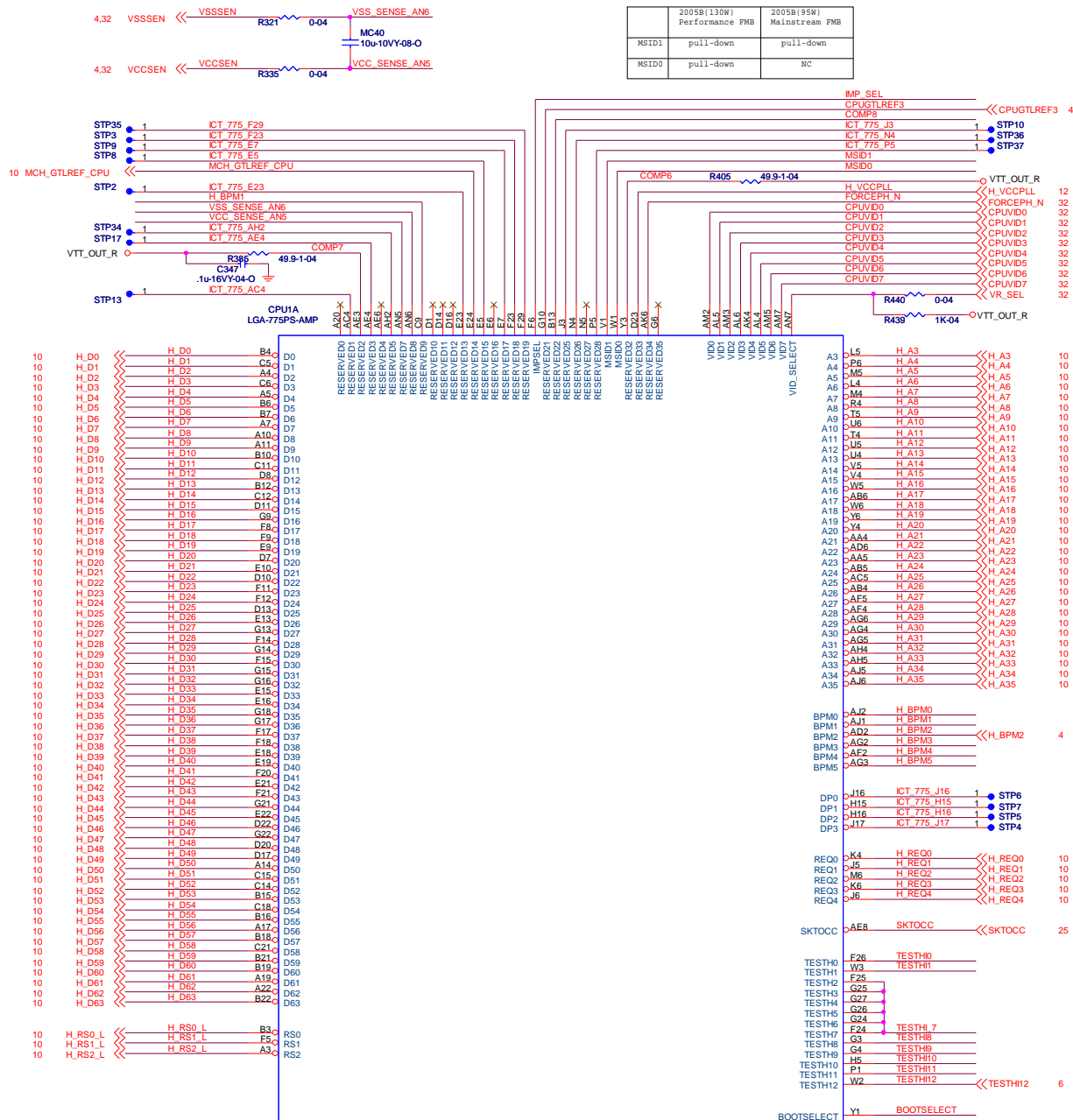
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DEVICE	IDSEL	INT#	REQ#	GNT#
PCI1	17	C/D/E/F	PREQ-0	PGNT-0
PCI2	18	D/E/F/G	PREQ-1	PGNT-1
1394	19	E	PREQ-2	PGNT-2

PCB : 244 x 244 mm ; 4 layers



System Block Diagram			
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Title

LGA 775 Part A

Size

Document Number

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Date

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Rev


1.0

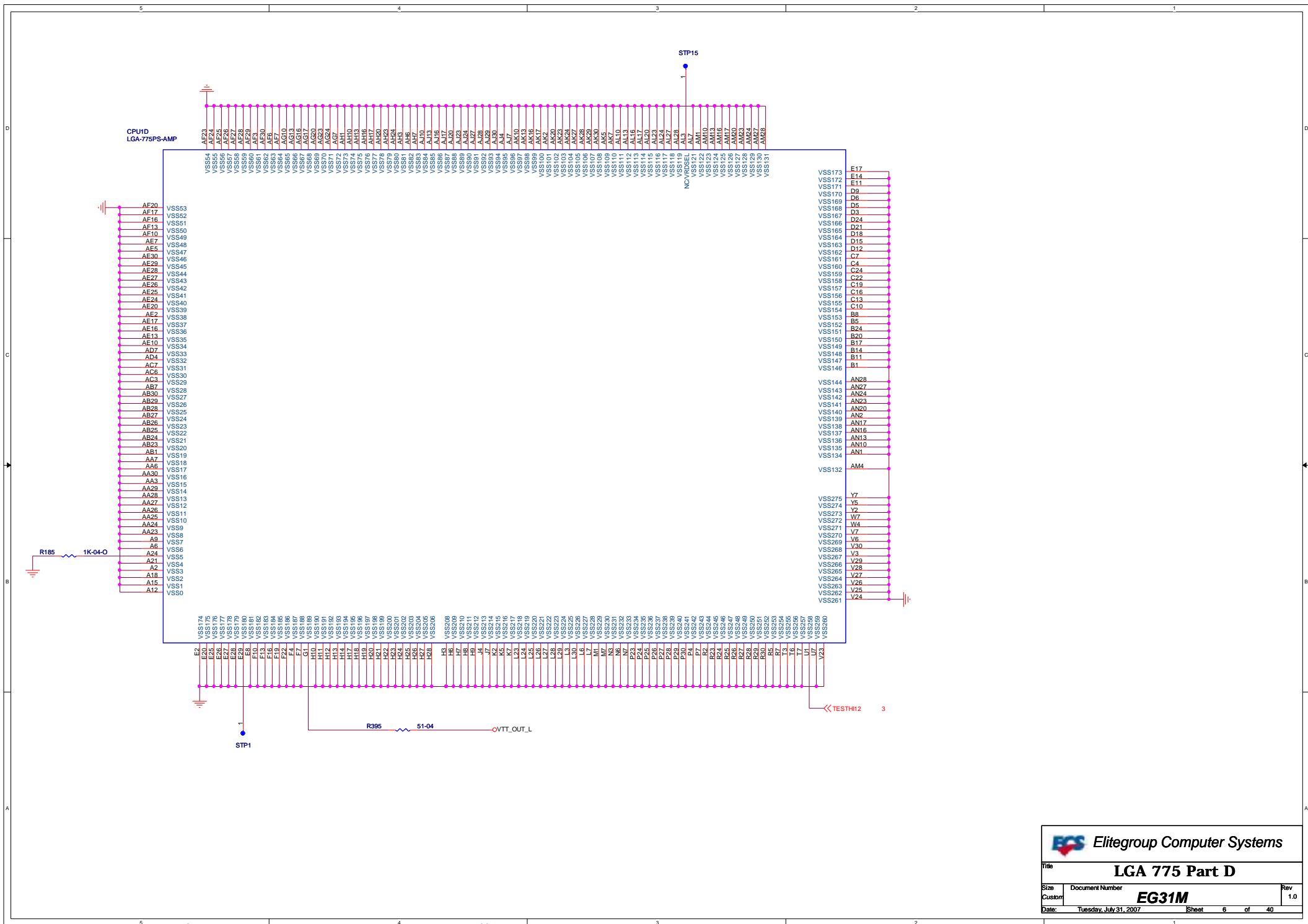
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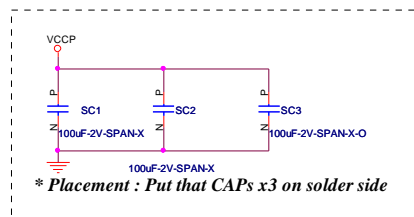
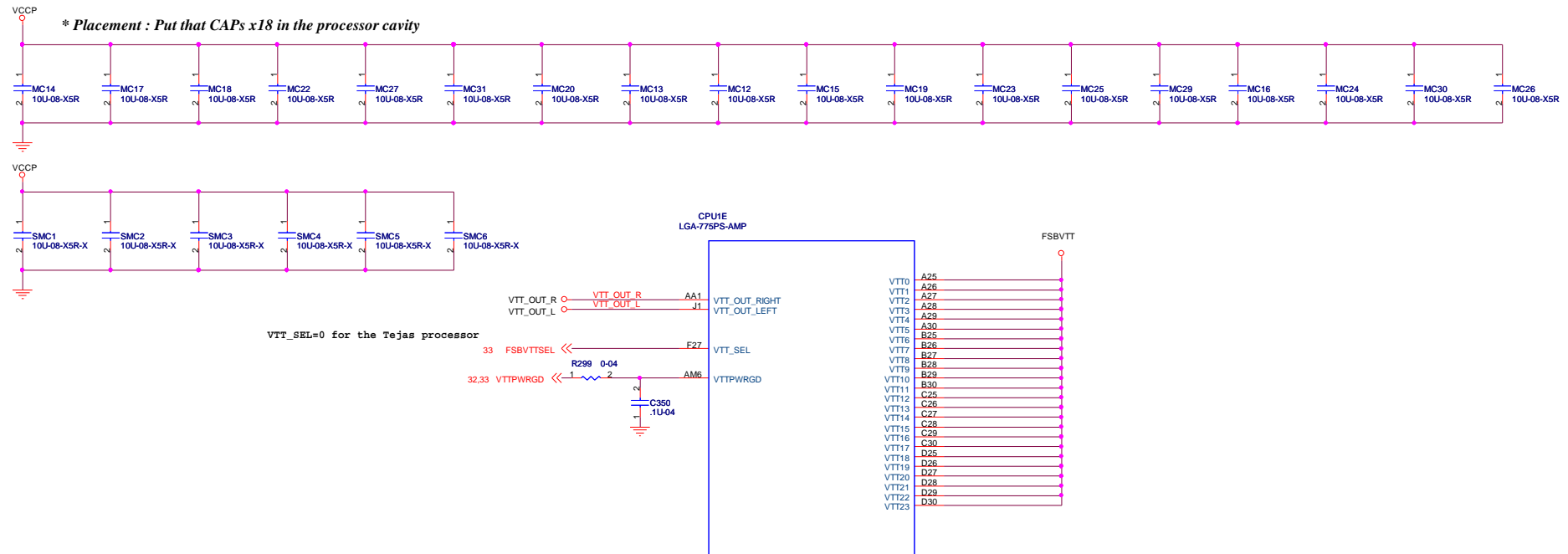
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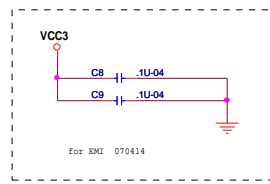
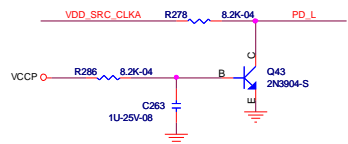
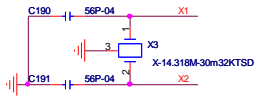
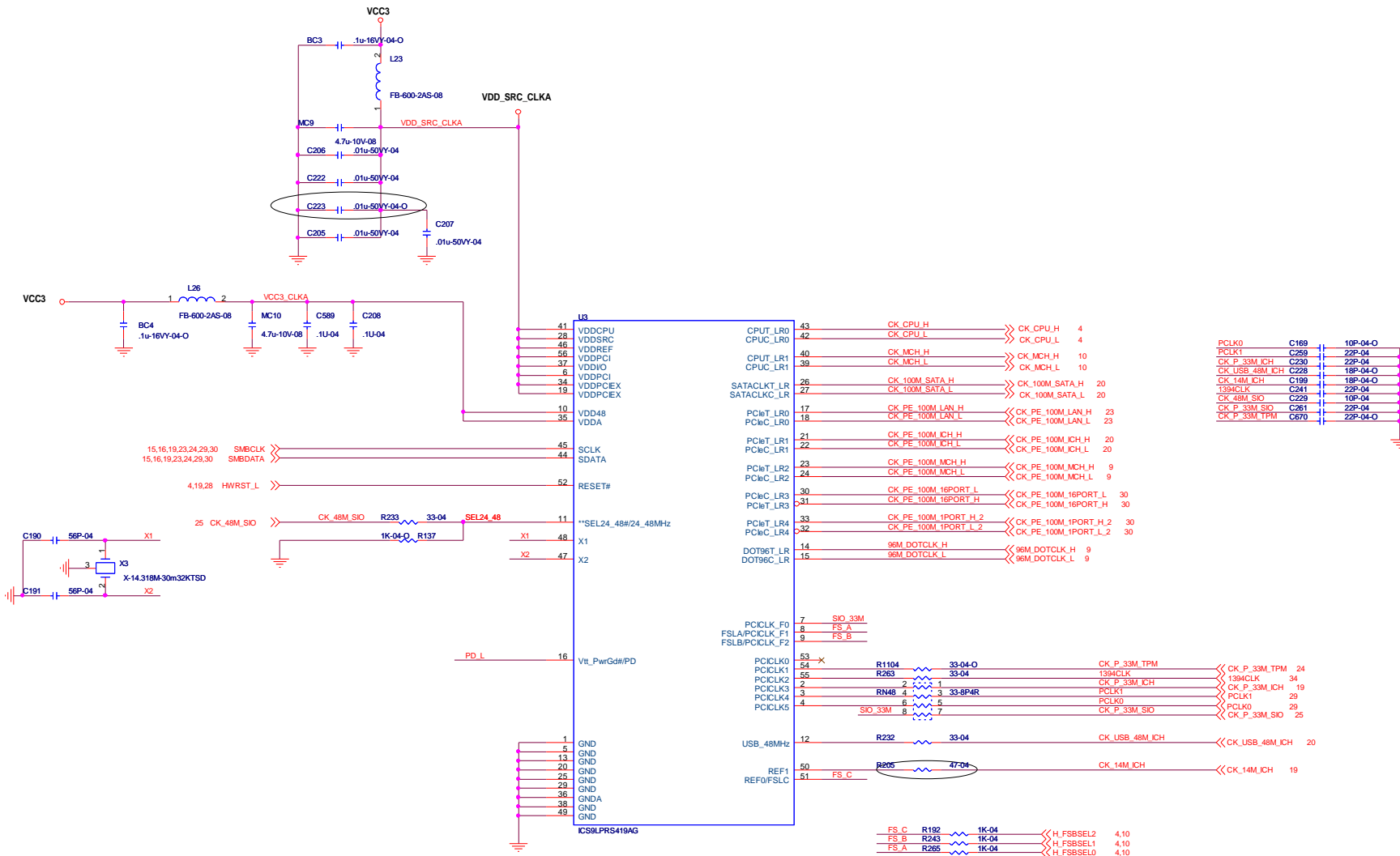


CPU1C
LGA-775PS-AMP

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For BSI 070414

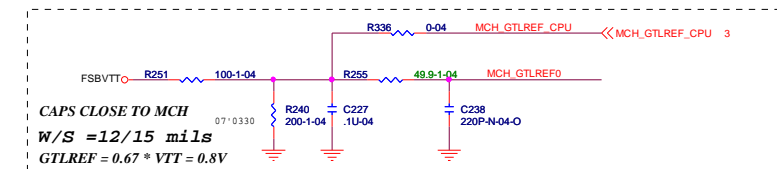
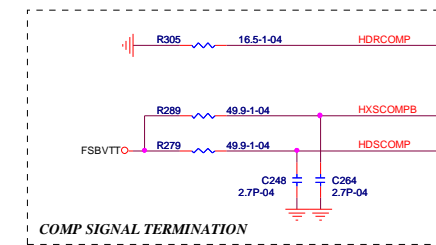
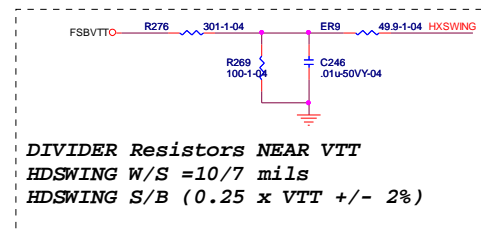
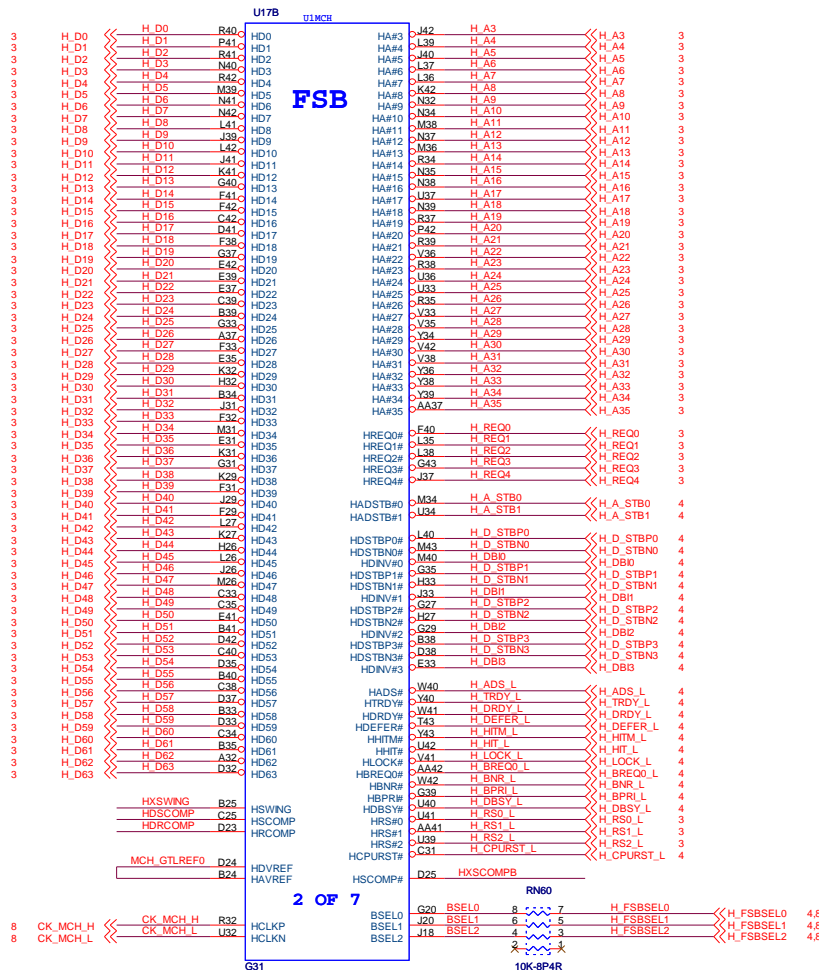
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0	0	0	266	MHz (1066)
0	1	0	200	MHz (800)

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Clock ICS LPRS419

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BSEL TABLE				
2	1	0	PSB	FREQUENCY
0	0	0	266	MHz (1066)
0	0	1	133	MHz (533)
0	1	0	200	MHz (800)

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BW(MCH) PartB

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U17C			
DOS H A4	AR41	SDQS_A4	SDQS_A0
DOS L A4	AR40	SDQS_A4#	SDQS_A0#
MPD A4	AU43	SDM_A4	SDM_A0
MD A32	AV42	SDQ_A32	SDQ_A0
MD A33	AV41	SDQ_A33	SDQ_A1
MD A34	AP42	SDQ_A34	SDQ_A2
MD A35	AN39	SDQ_A35	SDQ_A3
MD A36	AV40	SDQ_A36	SDQ_A4
MD A37	AV41	SDQ_A37	SDQ_A5
MD A38	AR42	SDQ_A38	SDQ_A6
MD A39	AP41	SDQ_A39	SDQ_A7
DOS H A5	AL41	SDQS_A5	SDQS_A1
DOS L A5	AL40	SDQS_A5#	SDQS_A1#
MPD A5	AM43	SDM_A5	SDM_A1
MD A40	AN41	SDQ_A40	SDQ_A8
MD A41	AM39	SDQ_A41	SDQ_A9
MD A42	AK42	SDQ_A42	SDQ_A10
MD A43	AK41	SDQ_A43	SDQ_A11
MD A44	AN40	SDQ_A44	SDQ_A12
MD A45	AN42	SDQ_A45	SDQ_A13
MD A46	AL42	SDQ_A46	SDQ_A14
MD A47	AL39	SDQ_A47	SDQ_A15
DOS H A6	AG42	SDQS_A6	SDQS_A2
DOS L A6	AG41	SDQS_A6#	SDQS_A2#
MPD A6	AG40	SDM_A6	SDM_A2
MD A48	AJ40	SDQ_A48	SDQ_A16
MD A49	AH43	SDQ_A49	SDQ_A17
MD A50	AF39	SDQ_A50	SDQ_A18
MD A51	AE40	SDQ_A51	SDQ_A19
MD A52	AJ42	SDQ_A52	SDQ_A20
MD A53	AJ41	SDQ_A53	SDQ_A21
MD A54	AF41	SDQ_A54	SDQ_A22
MD A55	AE42	SDQ_A55	SDQ_A23
DOS H A7	AC42	SDQS_A7	SDQS_A3
DOS L A7	AC41	SDQS_A7#	SDQS_A3#
MPD A7	AC40	SDM_A7	SDM_A3
MD A56	AD40	SDQ_A56	SDQ_A24
MD A57	AD43	SDQ_A57	SDQ_A25
MD A58	AB41	SDQ_A58	SDQ_A26
MD A59	AA40	SDQ_A59	SDQ_A27
MD A60	AE42	SDQ_A60	SDQ_A28
MD A61	AE41	SDQ_A61	SDQ_A29
MD A62	AC39	SDQ_A62	SDQ_A30
MD A63	AB42	SDQ_A63	SDQ_A31
MAAA 0	BA31	SMA_A0	SWE_A#
MAAA 1	BB25	SMA_A1	SCAS_A#
MAAA 2	BA26	SMA_A2	SRAS_A#
MAAA 3	BA25	SMA_A3	SBS_A0
MAAA 4	AY25	SMA_A4	SBS_A1
MAAA 5	BA23	SMA_A5	SBS_A2
MAAA 6	AY24	SMA_A6	SCS_A0#
MAAA 7	AY23	SMA_A7	SCS_A1#
MAAA 8	BB23	SMA_A8	SCS_A2#
MAAA 9	BA22	SMA_A9	SCS_A3#
MAAA 10	AY23	SMA_A10	SCKE_A0
MAAA 11	BB22	SMA_A11	SCKE_A1
MAAA 12	AW21	SMA_A12	SCKE_A2
MAAA 13	AY23	SMA_A13	SCKE_A3
MAAA 14	BA21	SMA_A14	RESERVED
DCLKA_H0	AU31	SCLK_A0	AN21X
DCLKA_L0	AR31	SCLK_A0#	
DCLKA_H1	AP27	SCLK_A1	
DCLKA_L1	AN27	SCLK_A1#	
DCLKA_H2	AV33	SCLK_A2	
DCLKA_L2	AW33	SCLK_A2#	
STP25	AP29	SCLK_A3	
STP31	AM26	SCLK_A4	
STP21	AM27	SCLK_A4#	
STP23	AT33	SCLK_A5	
STP26	AT33	SCLK_A5#	
STP24	AU33	SCLK_A5#	

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RESERVED

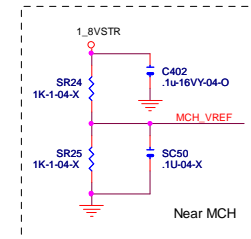
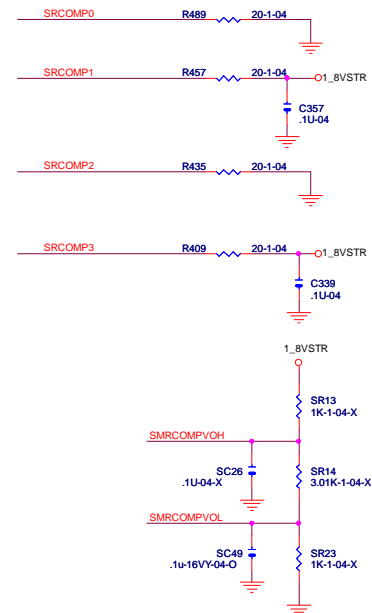
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DOS L B4	AU39	SDQS_B4#	SDQS_B0#
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MD B32	AW37	SDQ_B32	SDQ_B0
MD B33	AV38	SDQ_B33	SDQ_B1
MD B34	AN36	SDQ_B34	SDQ_B2
MD B35	AN37	SDQ_B35	SDQ_B3
MD B36	AU35	SDQ_B36	SDQ_B4
MD B37	AR35	SDQ_B37	SDQ_B5
MD B38	AN35	SDQ_B38	SDQ_B6
MD B39	AR37	SDQ_B39	SDQ_B7
DOS H B5	AL35	SDQS_B5	SDQS_B1
DOS L B5	AL34	SDQS_B5#	SDQS_B1#
MPD B5	AM37	SDM_B5	SDM_B1
MD B40	AM35	SDQ_B40	SDQ_B8
MD B41	AM38	SDQ_B41	SDQ_B9
MD B42	AJ34	SDQ_B42	SDQ_B10
MD B43	AL38	SDQ_B43	SDQ_B11
MD B44	AR39	SDQ_B44	SDQ_B12
MD B45	AM34	SDQ_B45	SDQ_B13
MD B46	AL37	SDQ_B46	SDQ_B14
MD B47	AL32	SDQ_B47	SDQ_B15
DOS H B6	AG35	SDQS_B6	SDQS_B2
DOS L B6	AG36	SDQS_B6#	SDQS_B2#
MPD B6	AG39	SDM_B6	SDM_B2
MD B48	AG38	SDQ_B48	SDQ_B16
MD B49	AJ38	SDQ_B49	SDQ_B17
MD B50	AF35	SDQ_B50	SDQ_B18
MD B51	AF33	SDQ_B51	SDQ_B19
MD B52	AJ37	SDQ_B52	SDQ_B20
MD B53	AJ35	SDQ_B53	SDQ_B21
MD B54	AG33	SDQ_B54	SDQ_B22
MD B55	AF34	SDQ_B55	SDQ_B23
DOS H B7	AC36	SDQS_B7	SDQS_B3
DOS L B7	AC37	SDQS_B7#	SDQS_B3#
MPD B7	AD39	SDM_B7	SDM_B3
MD B56	AD36	SDQ_B56	SDQ_B24
MD B57	AC33	SDQ_B57	SDQ_B25
MD B58	AA34	SDQ_B58	SDQ_B26
MD B59	AA36	SDQ_B59	SDQ_B27
MD B60	AD34	SDQ_B60	SDQ_B28
MD B61	AF38	SDQ_B61	SDQ_B29
MD B62	AC34	SDQ_B62	SDQ_B30
MD B63	AA33	SDQ_B63	SDQ_B31
MAAB 0	BB17	SMA_B0	SWE_B#
MAAB 1	AY17	SMA_B1	SCAS_B#
MAAB 2	BA17	SMA_B2	SRAS_B#
MAAB 3	BC16	SMA_B3	SBS_B0
MAAB 4	AW15	SMA_B4	SBS_B1
MAAB 5	BA15	SMA_B5	SBS_B2
MAAB 6	BB15	SMA_B6	SCS_B0#
MAAB 7	BA14	SMA_B7	SCS_B1#
MAAB 8	AY15	SMA_B8	SCS_B2#
MAAB 9	BB14	SMA_B9	SCS_B3#
MAAB 10	AW18	SMA_B10	SCKE_B0
MAAB 11	BB13	SMA_B11	SCKE_B1
MAAB 12	BA13	SMA_B12	SCKE_B2
MAAB 13	AY29	SMA_B13	SCKE_B3
MAAB 14	AY13	SMA_B14	RESERVED
DCLKB_H0	AV31	SCLK_B0	AN21X
DCLKB_L0	AW31	SCLK_B0#	
DCLKB_H1	AU27	SCLK_B1	
DCLKB_L1	AT27	SCLK_B1#	
DCLKB_H2	AV32	SCLK_B2	
DCLKB_L2	AT32	SCLK_B2#	
STP28	1 AU29	SCLK_B3	
STP33	1 AR29	SCLK_B3#	
STP31	1 AV29	SCLK_B4	
STP30	1 AM27	SCLK_B4#	
STP20	1 AN33	SCLK_B5	
STP22	1 AP22	SCLK_B5#	
RESERVED	BB2	RESERVED	
RESERVED	AW42	RESERVED	
RESERVED	AN32	RESERVED	
RESERVED	AM31	RESERVED	
RESERVED	AG32	RESERVED	
RESERVED	AE32	RESERVED	
RESERVED	BB19	RESERVED	
RESERVED	RESERVED	RESERVED	

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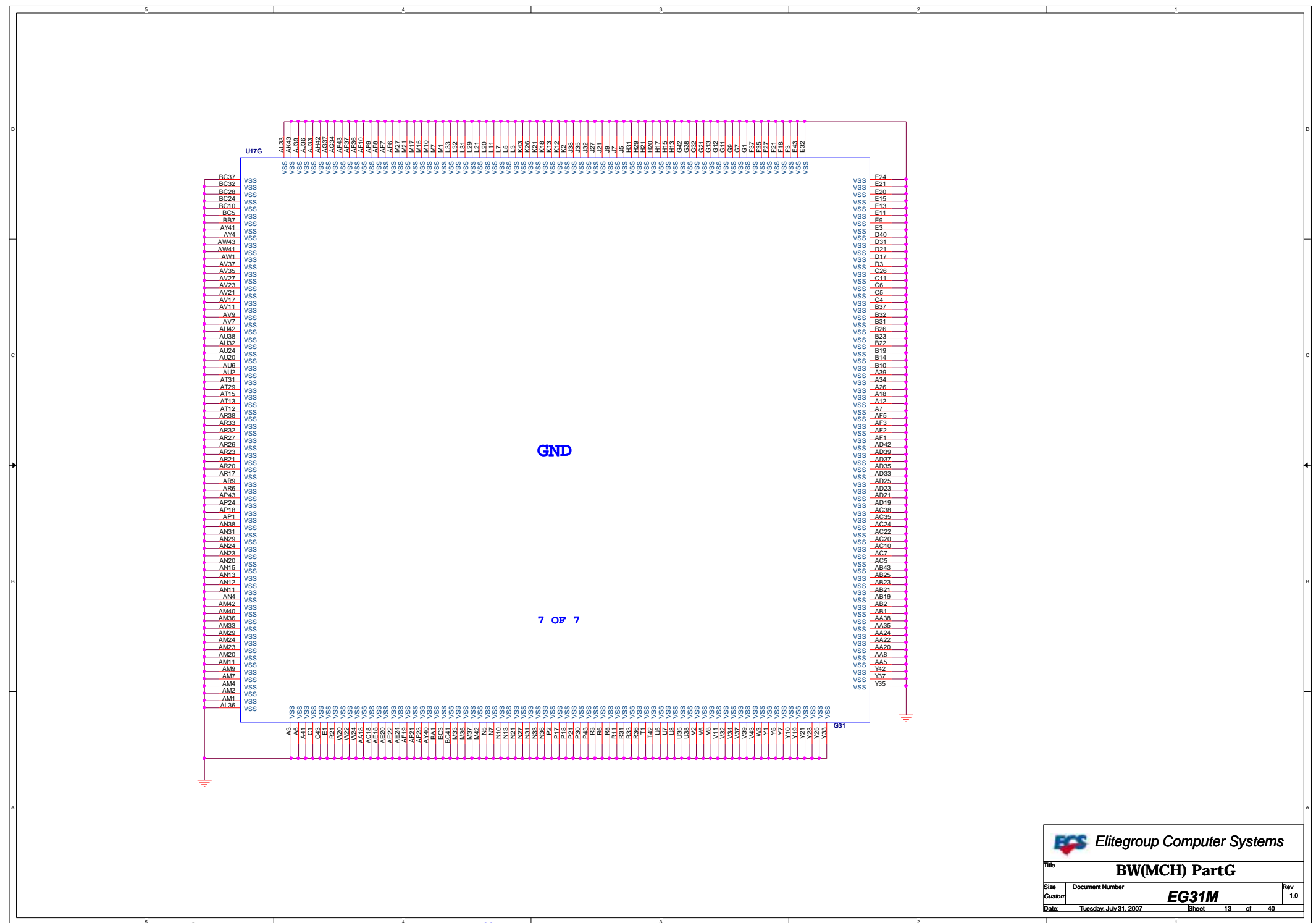
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DDR_1

15 MD_A[0..63]	MD_A[0..63]
15 DOS_L_A[0..7]	DOS_L_A[0..7]
15 DOS_H_A[0..7]	DOS_H_A[0..7]
15 MPD_A[0..7]	MPD_A[0..7]
15 MAAA_0[0..14]	MAAA_0[0..14]
15 CSA_L[0..1]	CSA_L[0..1]
15 CKEA_0[0..1]	CKEA_0[0..1]
15 MD_B[0..63]	MD_B[0..63]
16 DOS_L_B[0..7]	DOS_L_B[0..7]
16 DOS_H_B[0..7]	DOS_H_B[0..7]
16 MPD_B[0..7]	MPD_B[0..7]
16 MAAA_B[0..14]	MAAA_B[0..14]
16 CSA_B[0..1]	CSA_B[0..1]
16 CKEB_0[0..1]	CKEB_0[0..1]
16 ODTB_0[0..1]	ODTB_0[0..1]
16 SSSB_0[0..2]	SSSB_0[0..2]
16 SWE_L_B	SWE_L_B
16 SCAS_L_B	SCAS_L_B
16 SRAS_L_B	SRAS_L_B
16 DCLKB_L[0..2]	DCLKB_L[0..2]
16 DCLKB_H[0..2]	DCLKB_H[0..2]
16 DCLKA_L[0..2]	DCLKA_L[0..2]
16 DCLKA_H[0..2]	DCLKA_H[0..2]

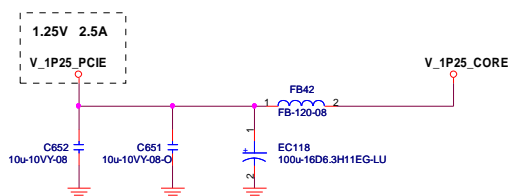
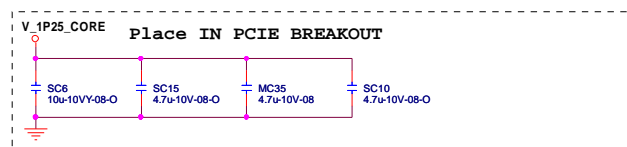
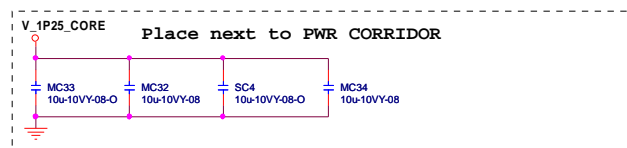
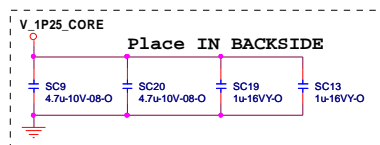
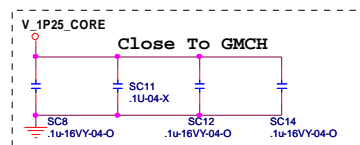
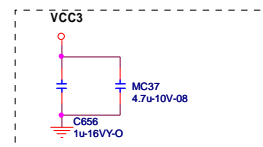
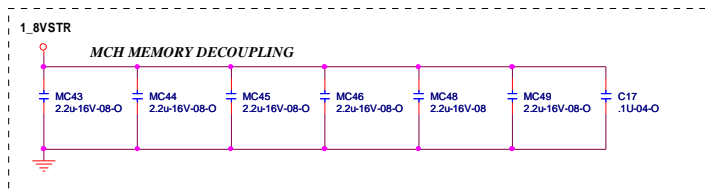
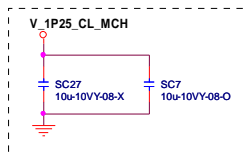
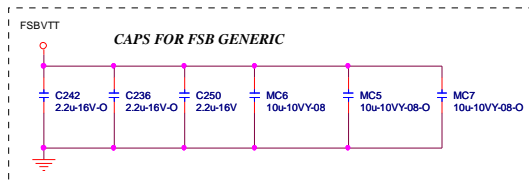


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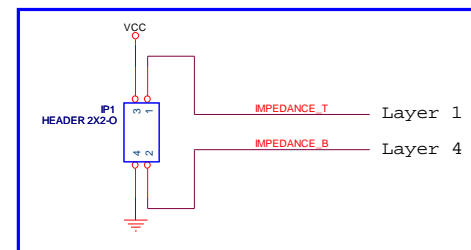
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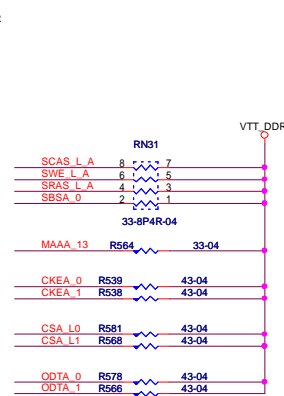
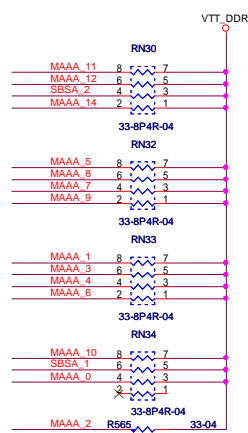
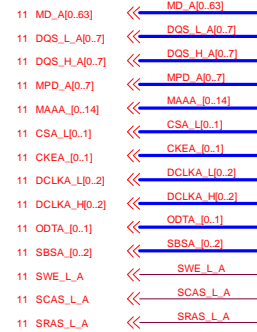
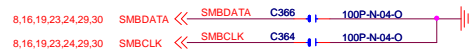
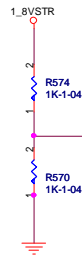
PCB Impedance control

Impedance (ohm)	Trace Width (mil)	(S/W/S)	Trace Length (inch)	Pre-preg
60	5	(20/5/20)	6	2116
50	4	(50/4/50)	6	1080
42	6	(50/6/50)	6	1080

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EGS Elitegroup Computer Systems			
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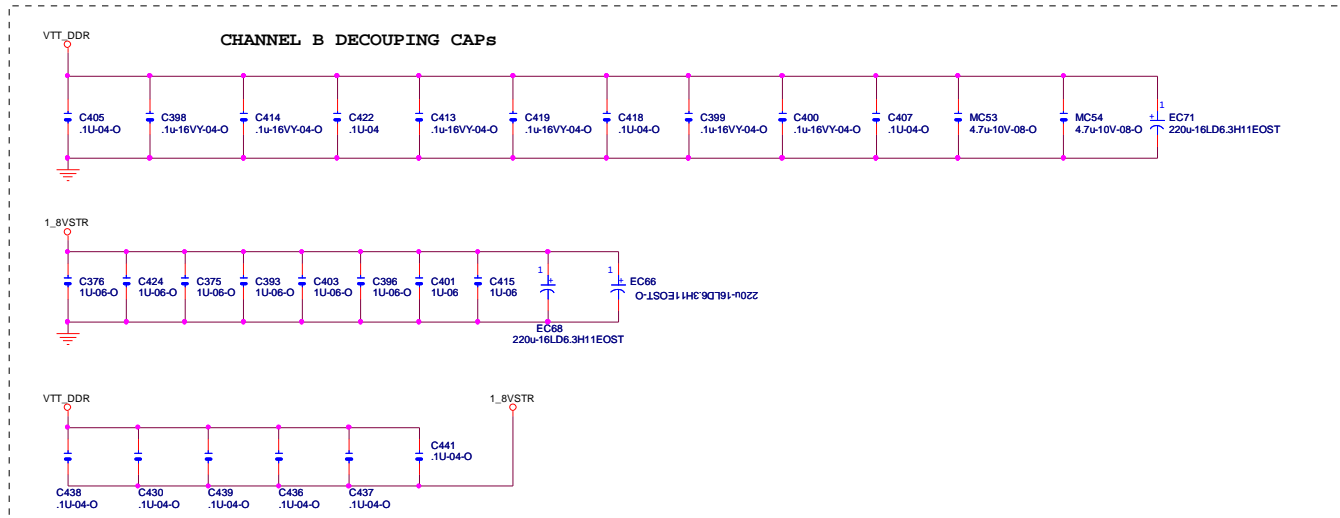
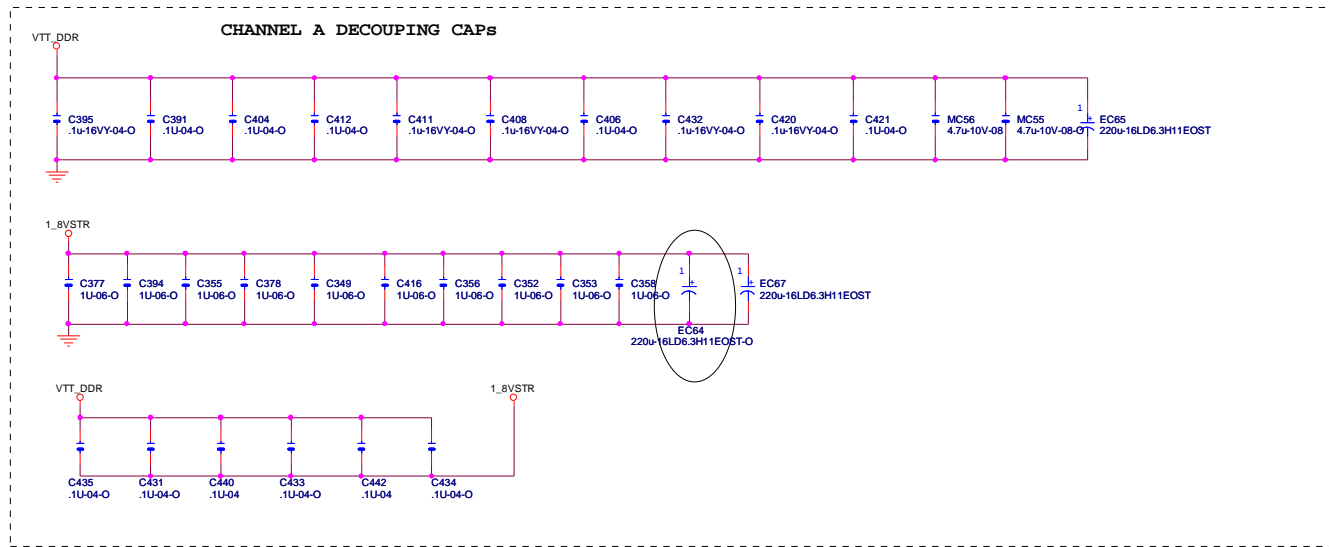



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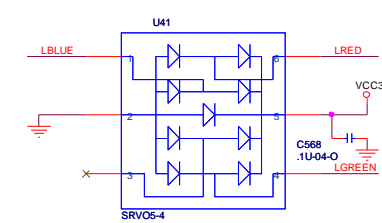
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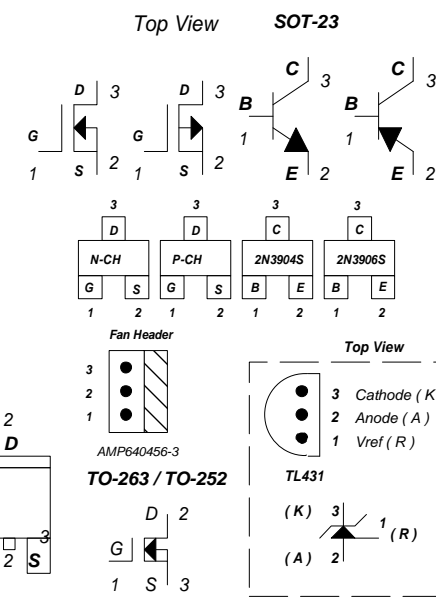
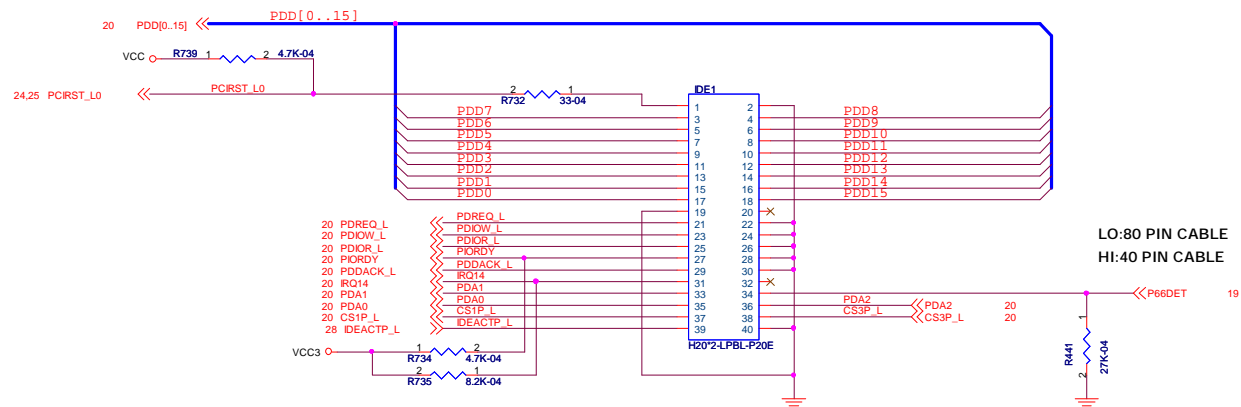
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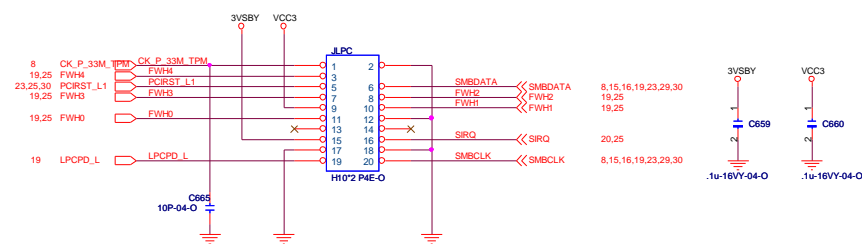
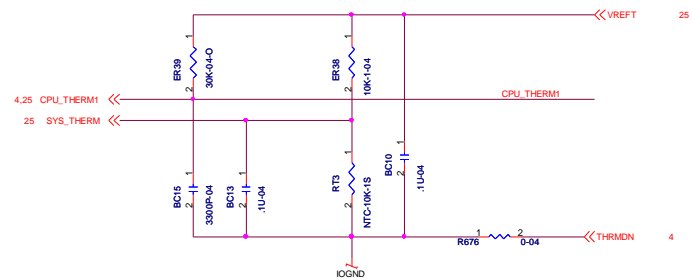
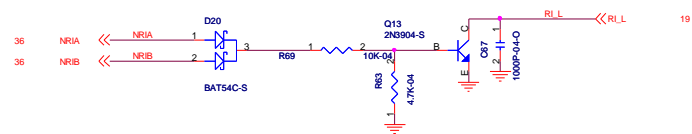
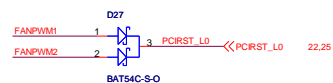
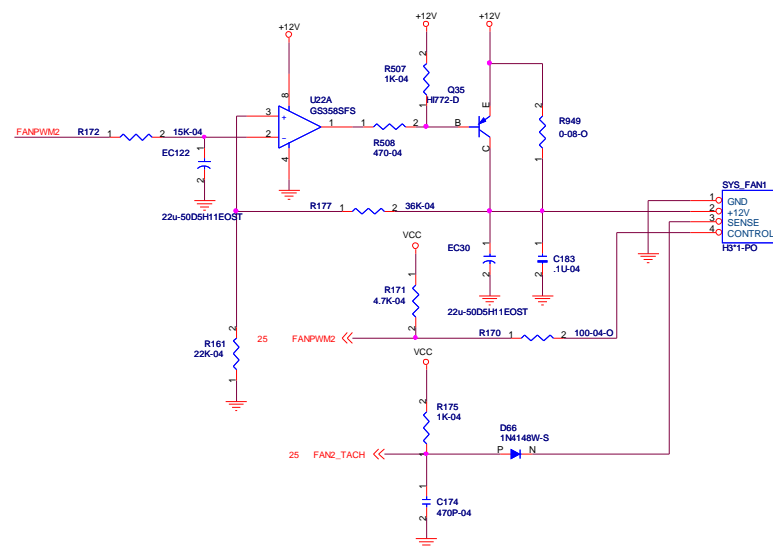
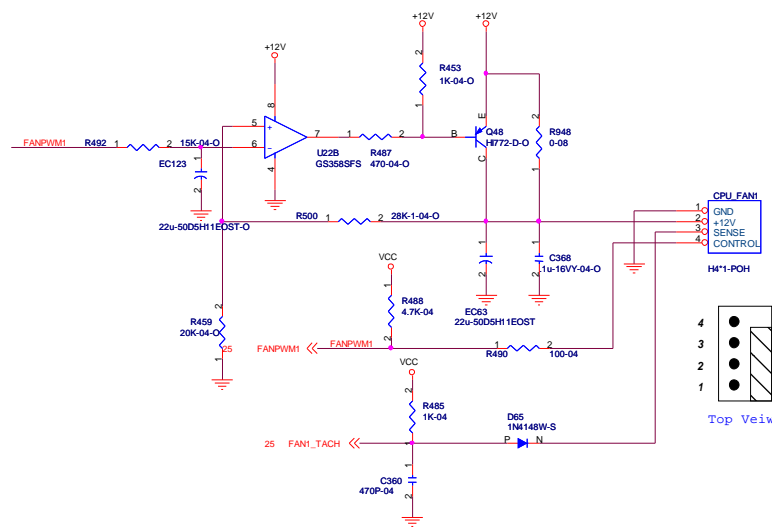
Date: **Tuesday, July 31, 2007** Sheet **15** of **40**

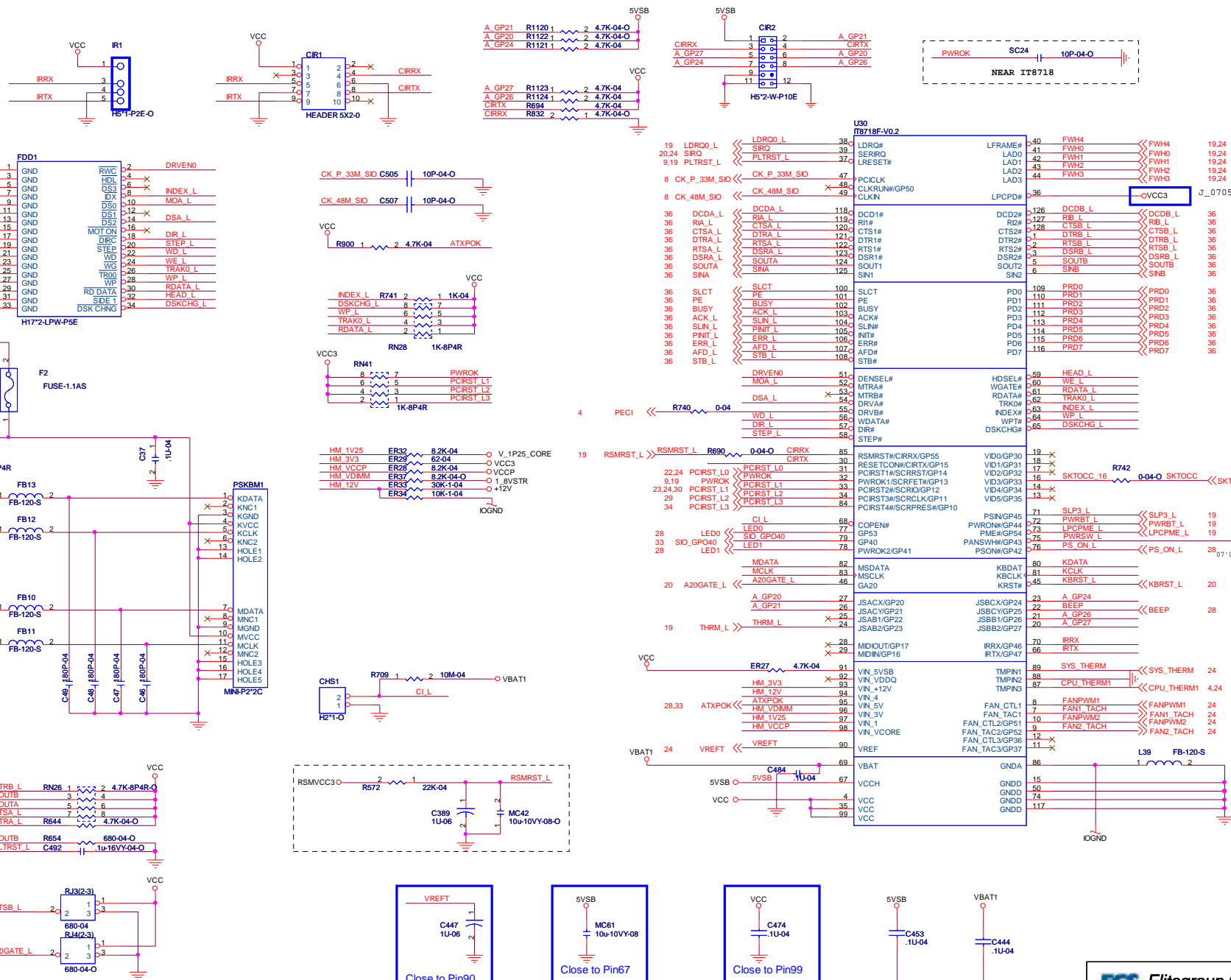


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Title		DIMM Decoupling	
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PIN2	PIN46	
1	1	100%
1	0	75%
0	1	50%
0	0	25%

High-4.7K
Low-680

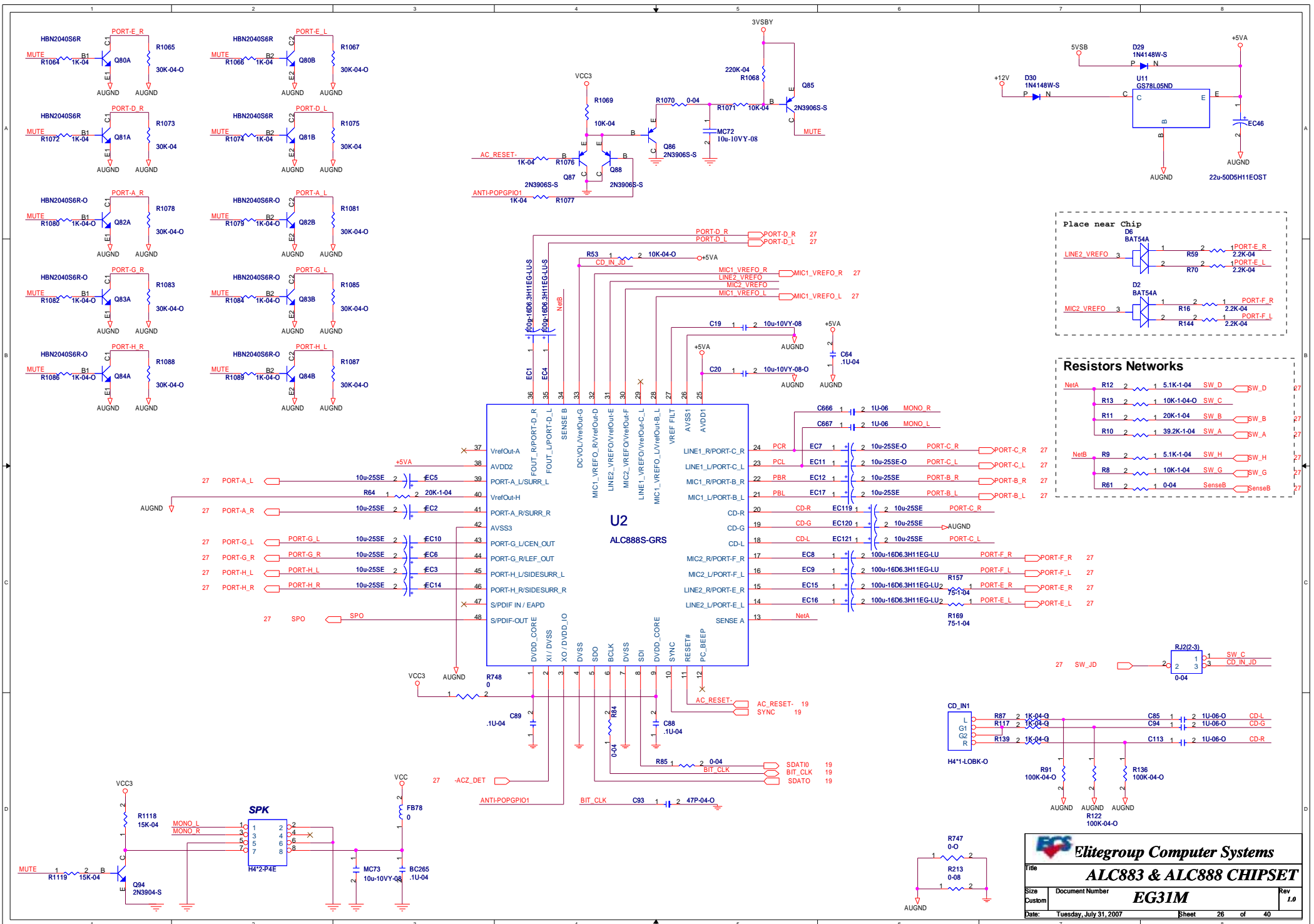
Elitegroup Computer Systems

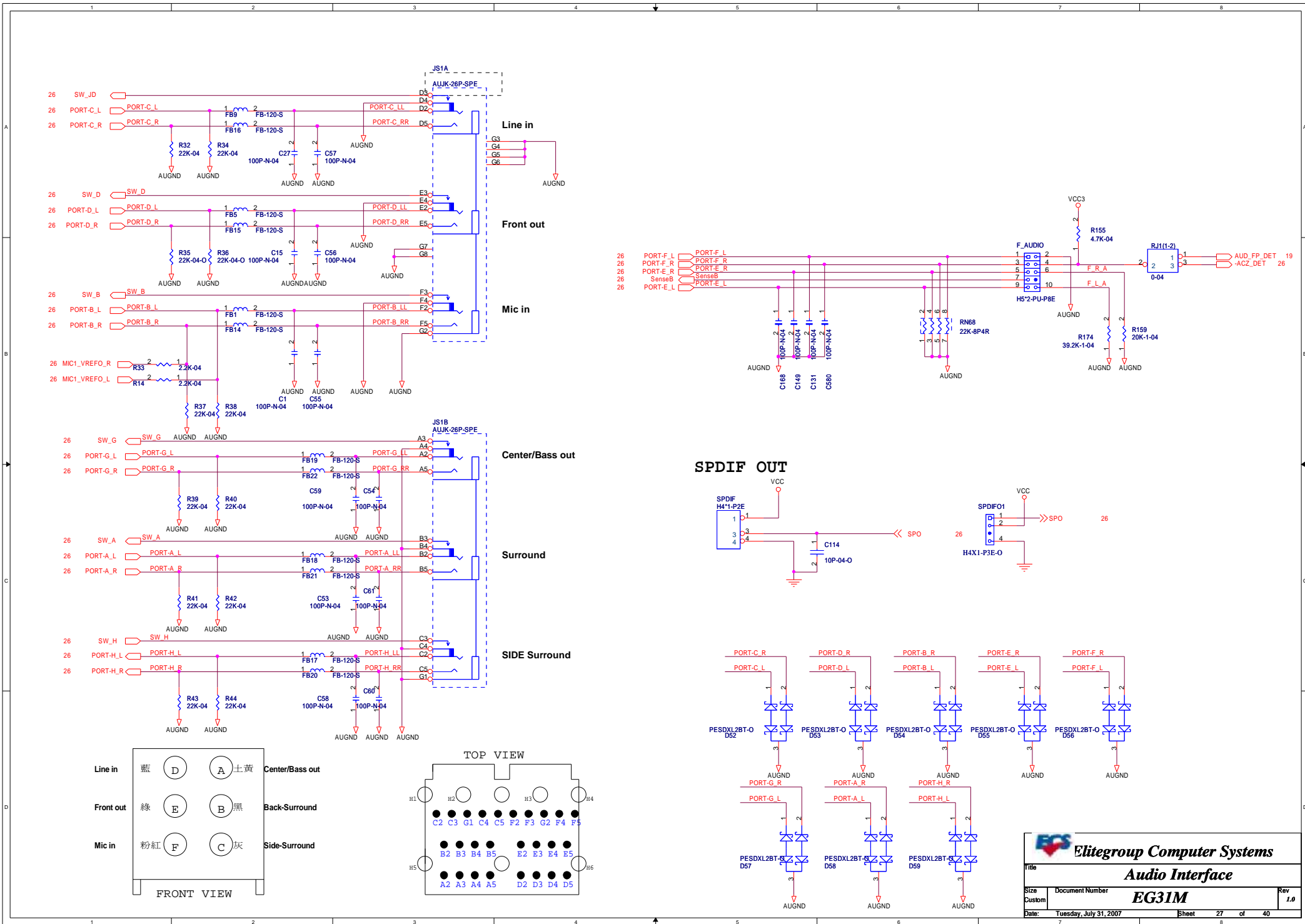
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Size Document Number **EG31M**

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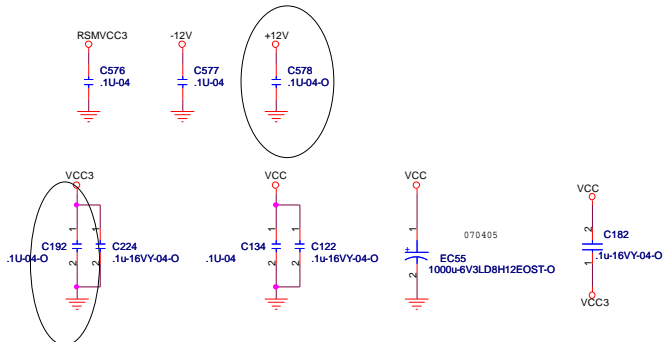
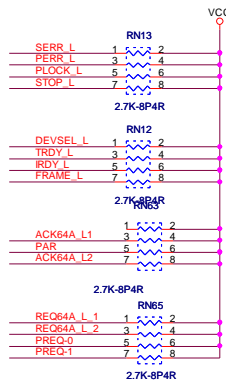
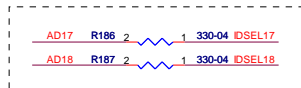
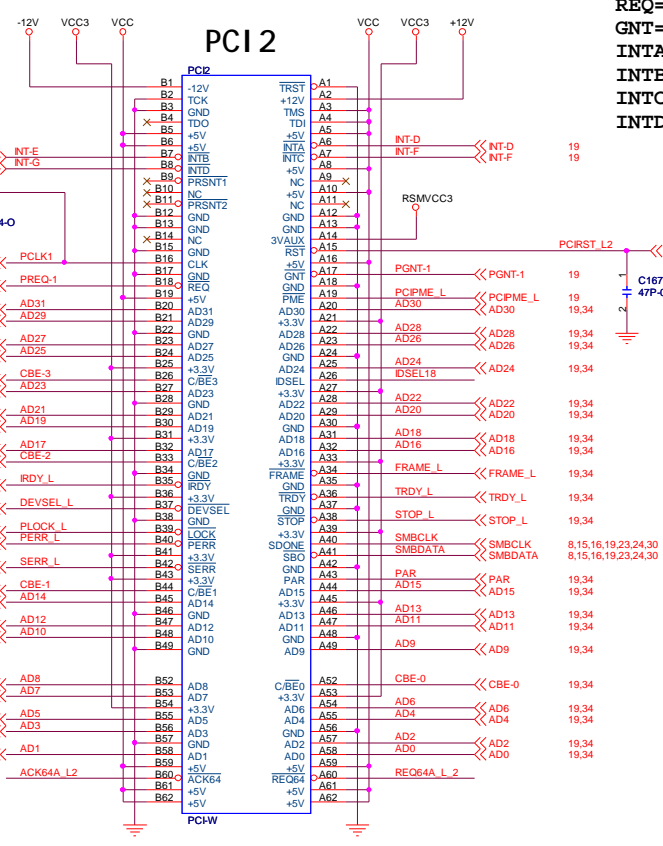
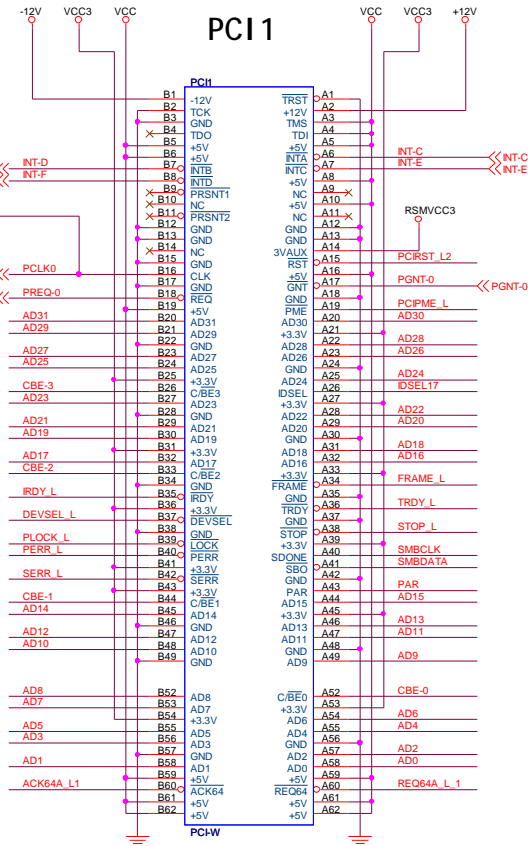
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RST=PCIRST_L2
IDSEL=AD17
REQ=REQ-0
GNT=PGNT-0
INTA: INT-C
INTB: INT-D
INTC: INT-E
INTD: INT-F

CLK=PCK1
RST=PCIRST_L2
IDSEL=AD18
REQ=REQ-1
GNT=PGNT-1
INTA: INT-D
INTB: INT-E
INTC: INT-F
INTD: INT-G

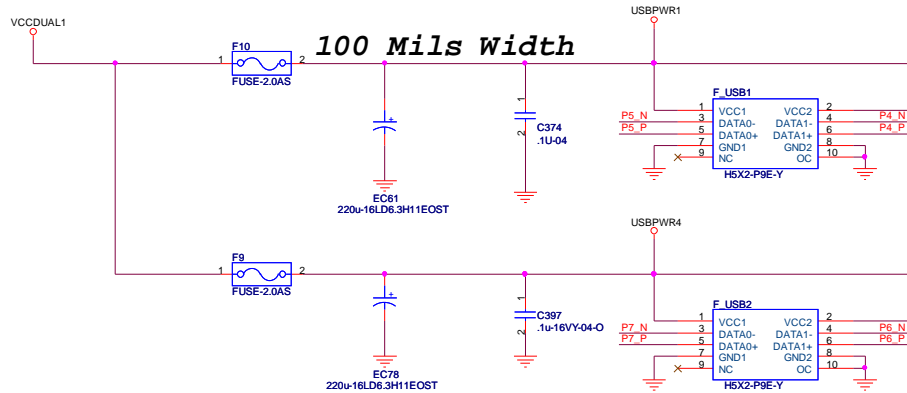


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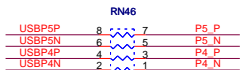
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Size: Custom Document Number: **EG31M** Rev: 1.0

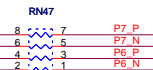
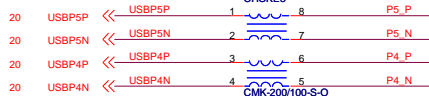
Date: Tuesday, July 31, 2007 Sheet: 29 of 40



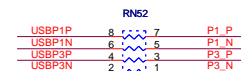
FRONT_SIDE



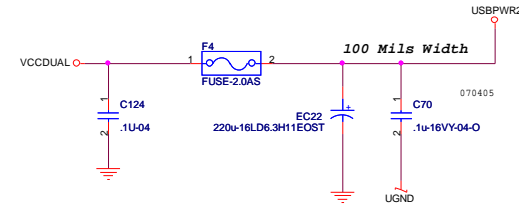
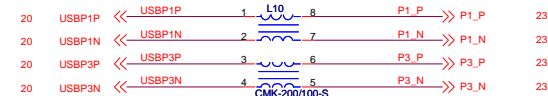
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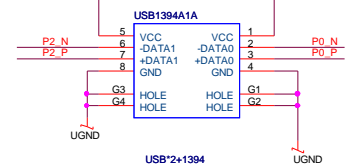
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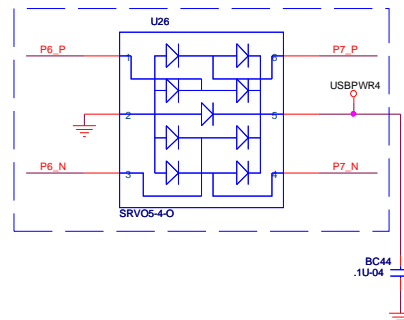
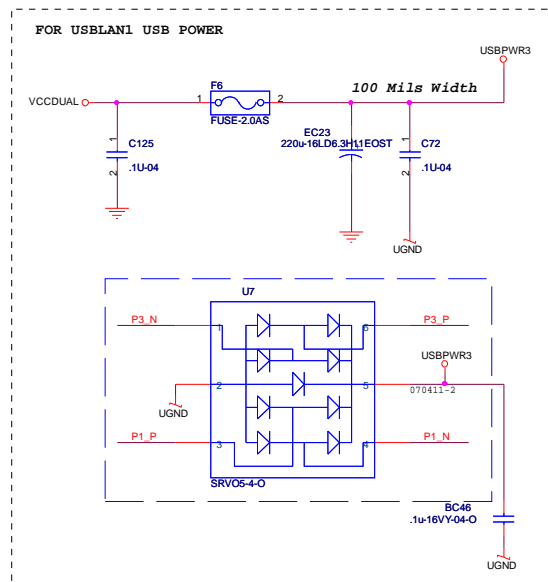
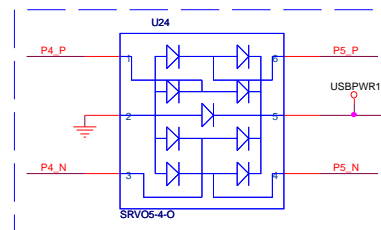
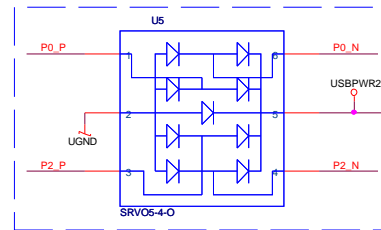
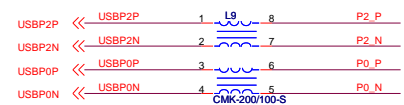
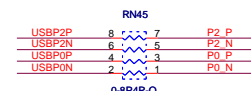
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REAR_SIDE



USB CONNECTOR



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Size: **EG31M**

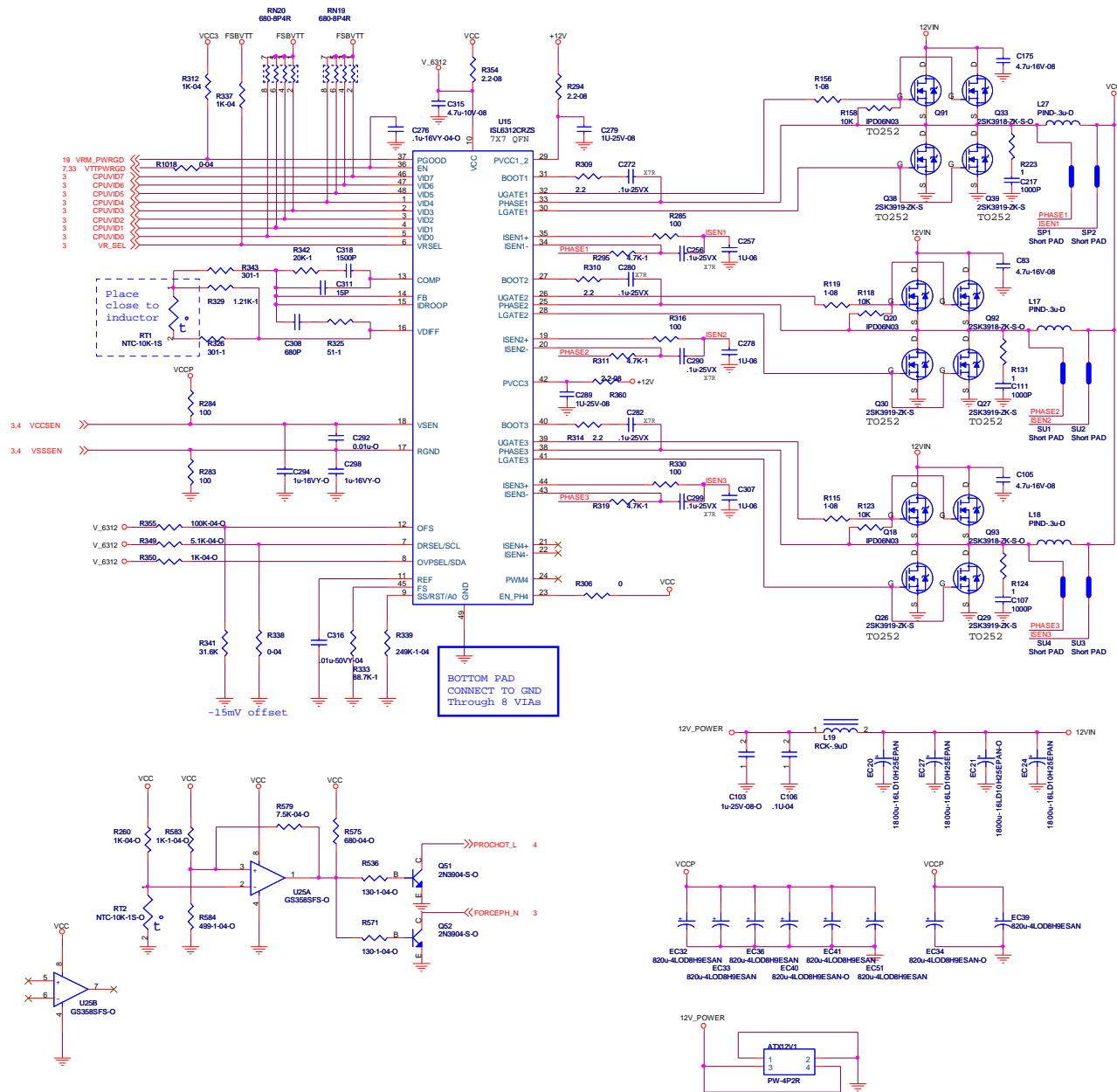
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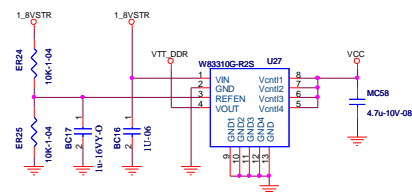
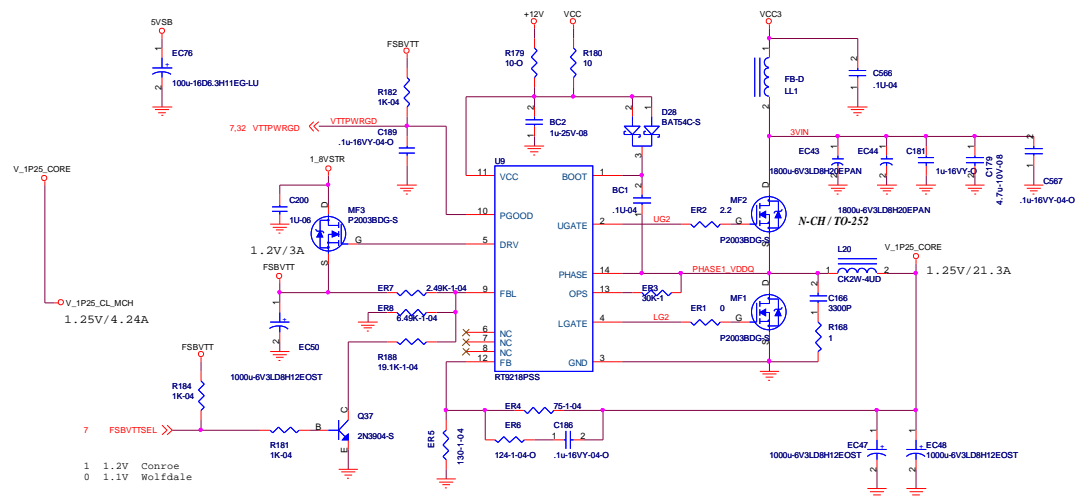
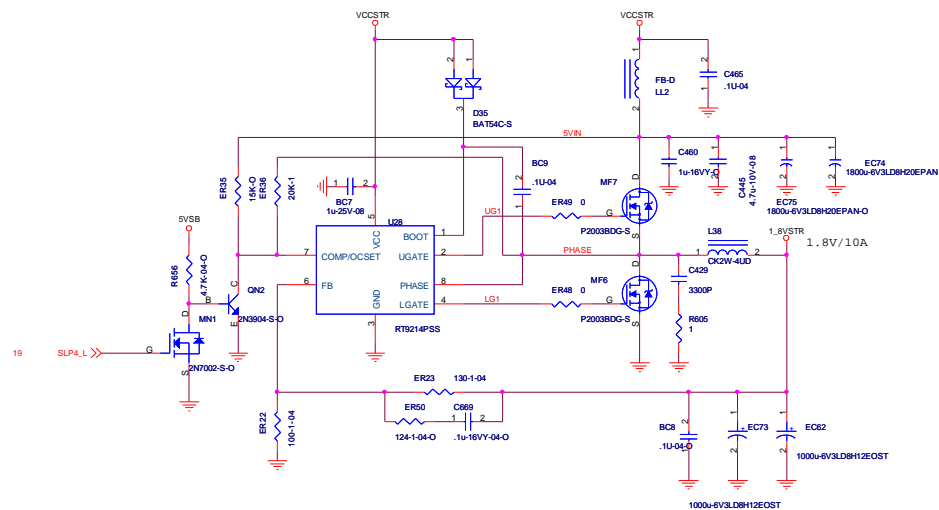
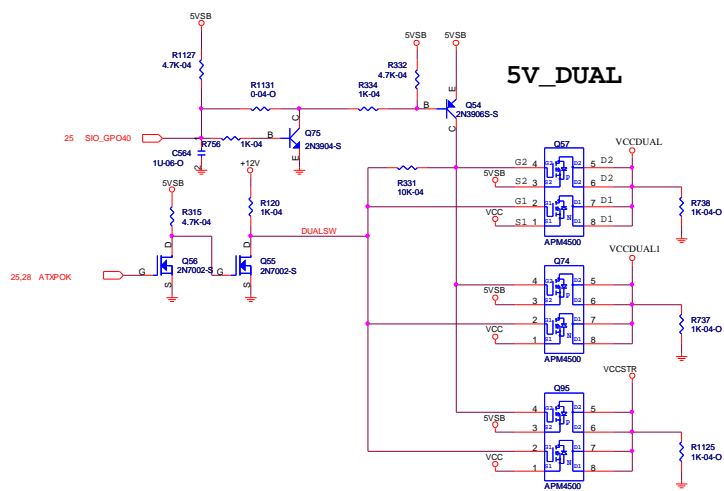
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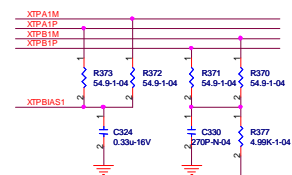
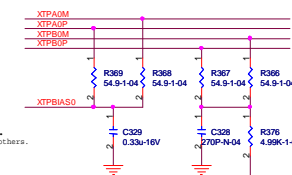
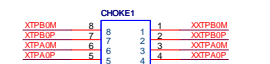
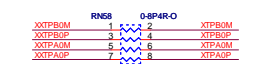
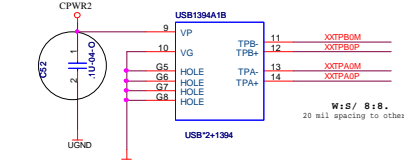
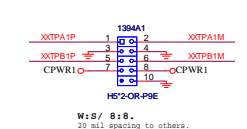
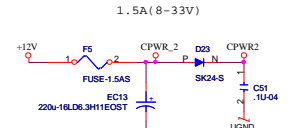
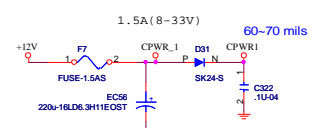
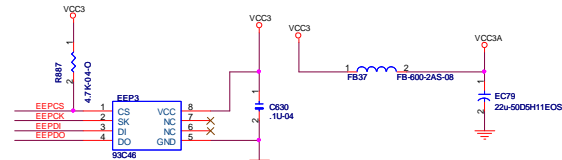




19.29 AD[0..31] << AD[0..31]

INT:E
IDSEL:AD19

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19.29 IRDY_L
19.29 TRDY_L
19.29 DEVSEL_L
19.29 STOP_L

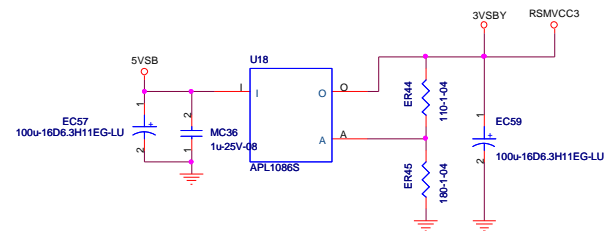
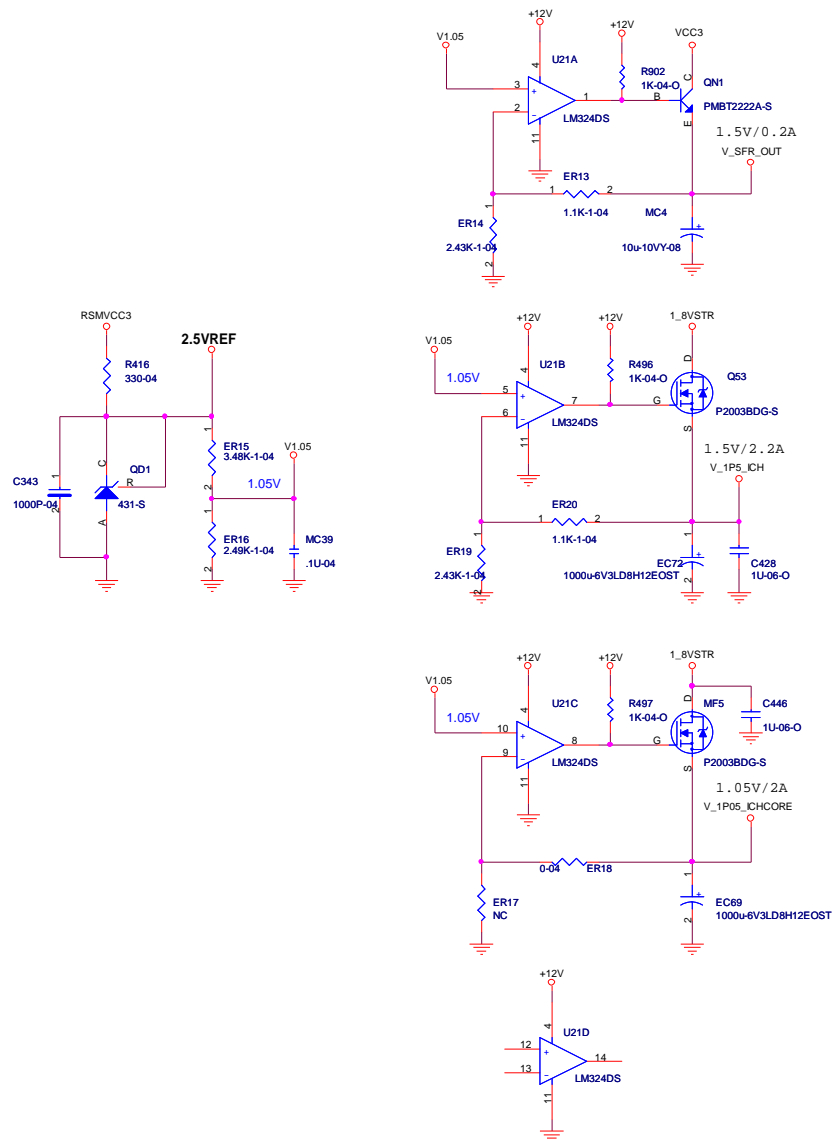



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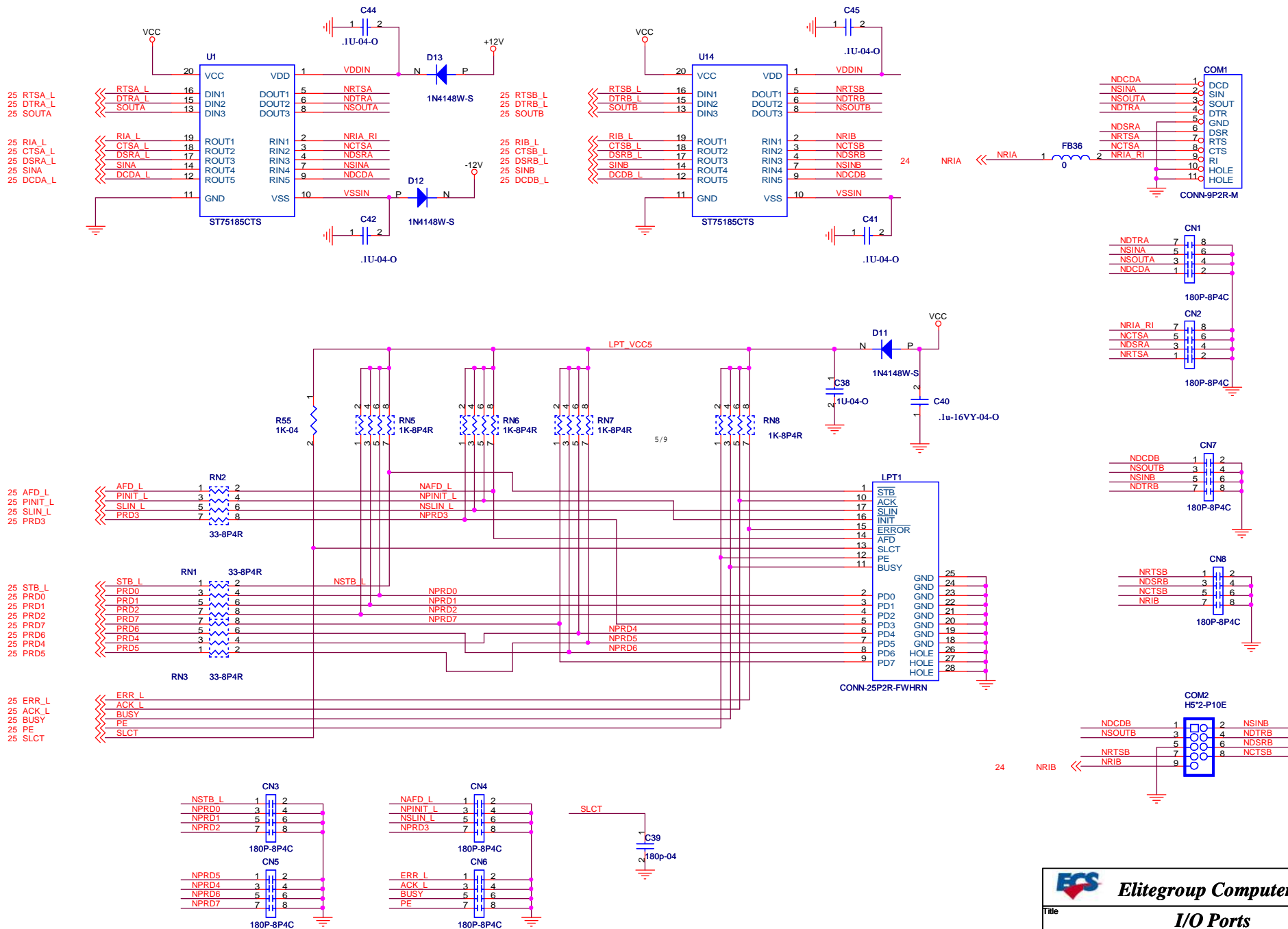
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Size Document Number EG31M

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Size Custom	Document Number	EG31M	Rev 1.0
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Title
I/O Ports

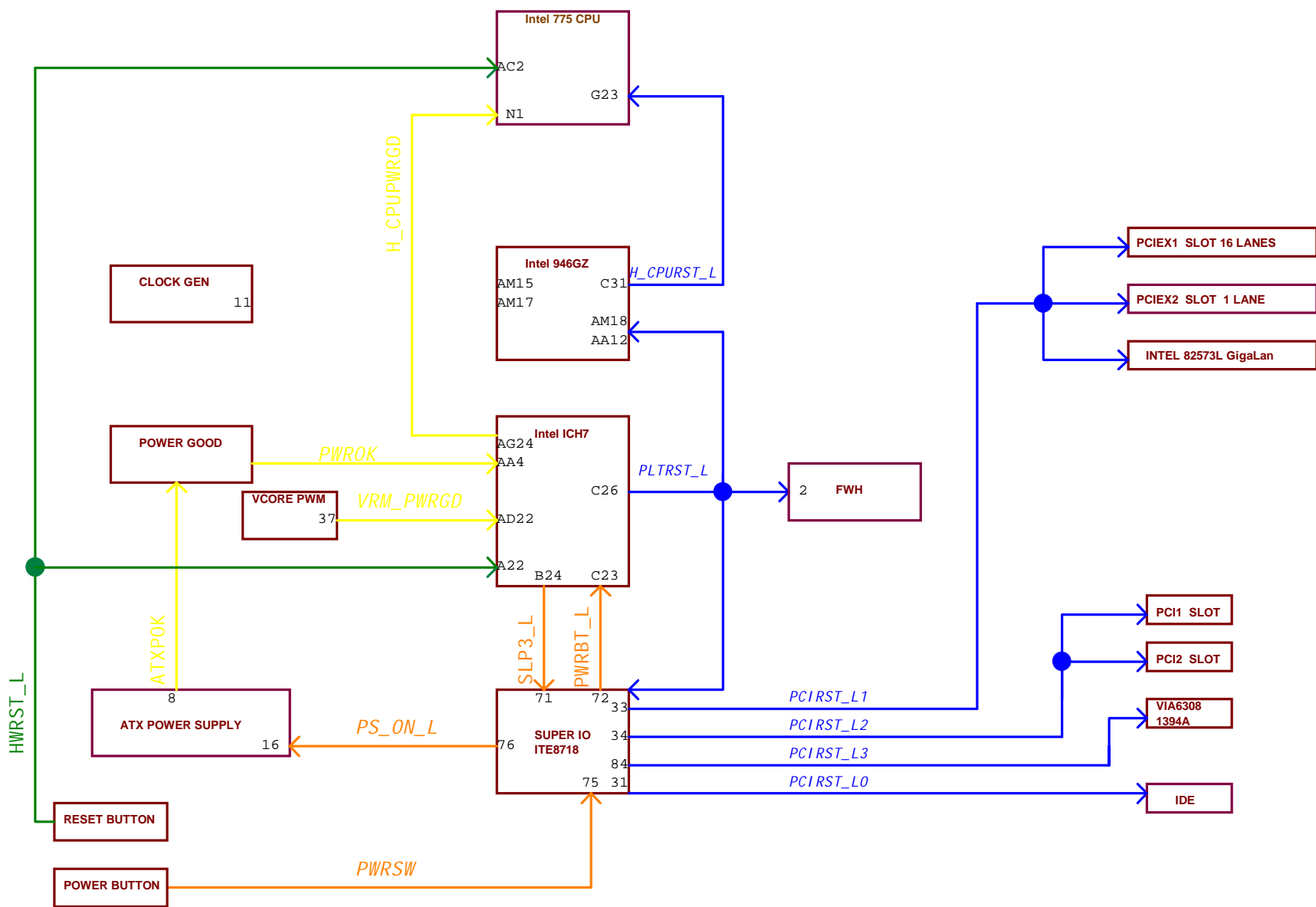
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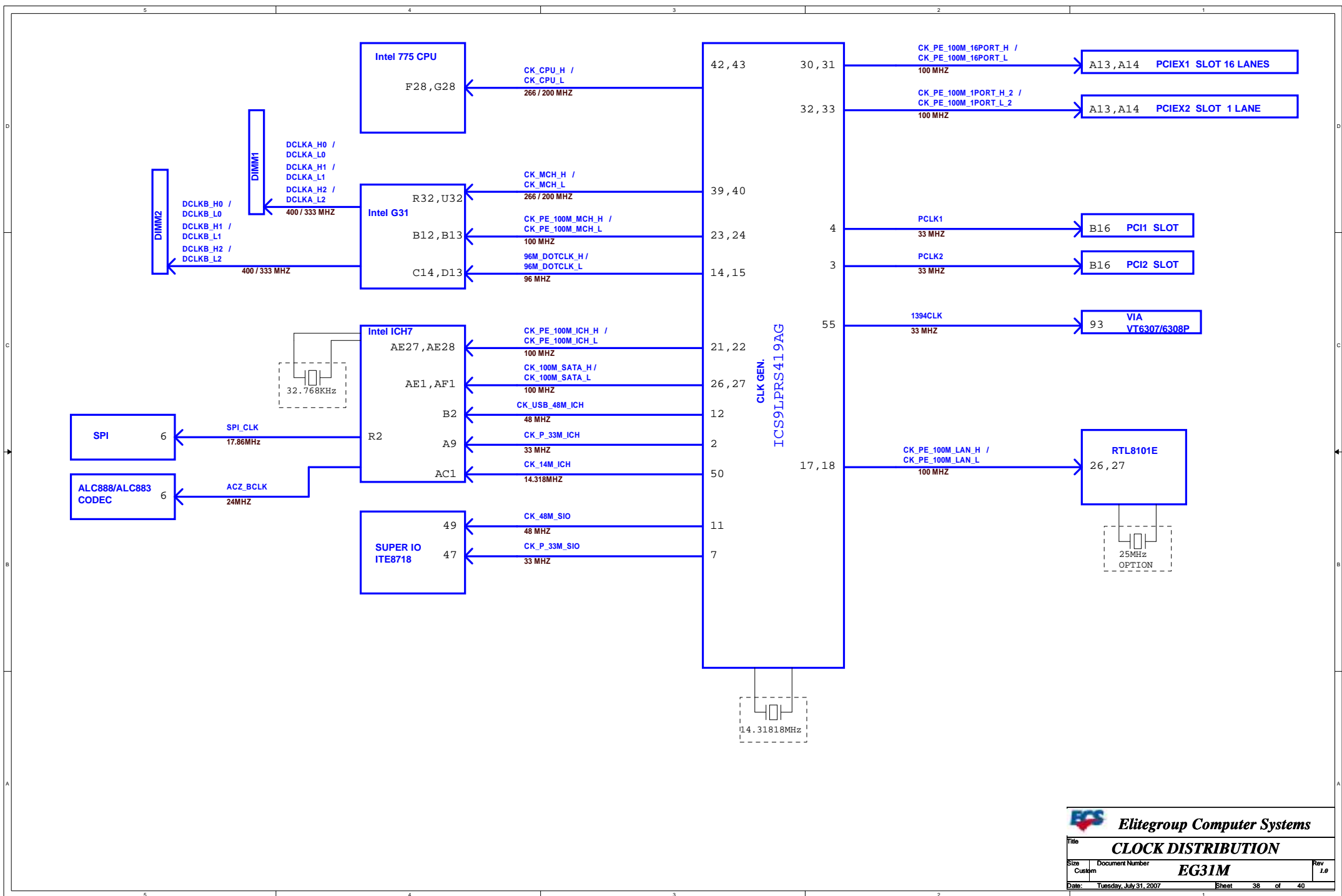
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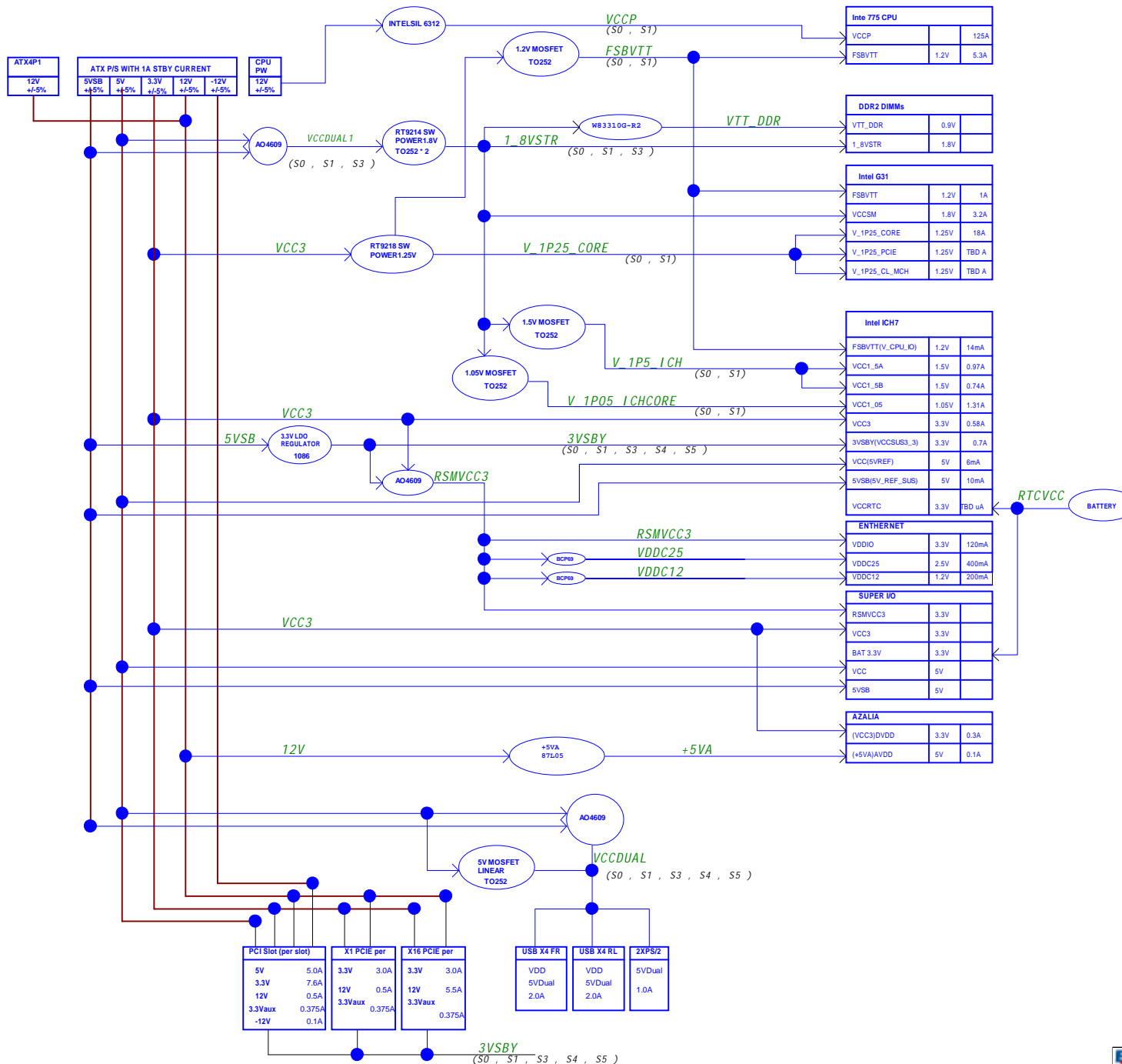
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Schematics Version History Table :

<i>Circuit Ver.</i>	<i>PCB Ver.</i>	<i>Total Page</i>	<i>Modified Page(s)</i>	<i>Date</i>
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Revision History			
Title Size Custom	Document Number EG31M		Rev 1.0
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